
2 KEY TOUCH PAD DETECTOR IC

GENERAL DESCRIPTION

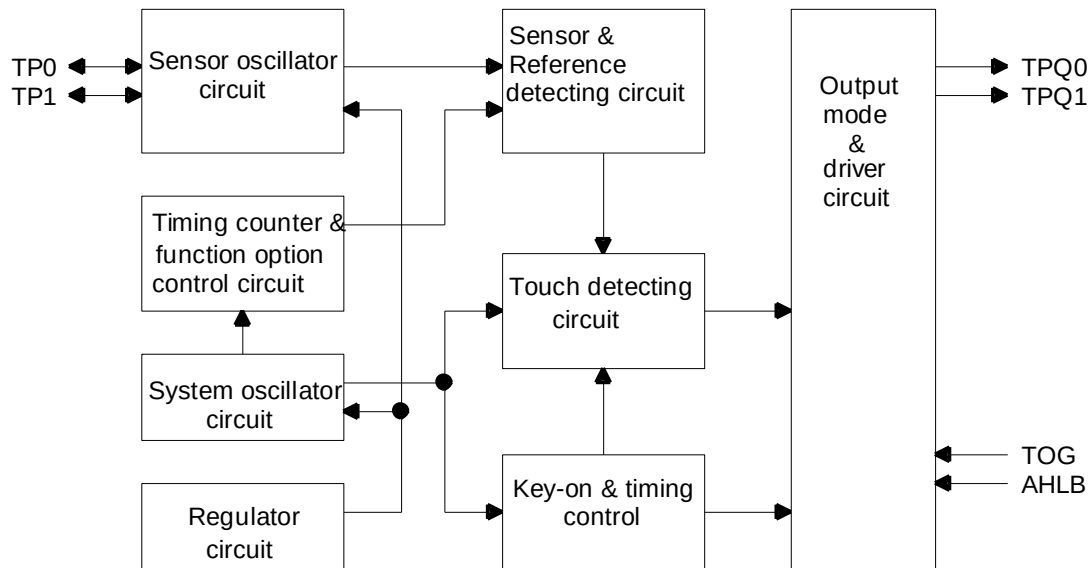
The TTP224N-CO8 TonTouch™ IC is capacitive sensing design specifically for touch pad controls. The device built in regulator for touch sensor. Stable sensing method can cover diversity conditions. Human interfaces control panel links through non-conductive dielectric material. The main application is focused at replacing of the mechanical switch or button. The ASSP can independently handle the 2 touch pads with 2 direct output pins.

FEATURES

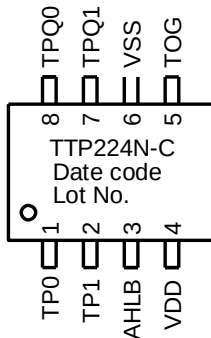
- Operating voltage 2.4V~5.5V
- Built-in regulator
- Operating current, @VDD=3V no load
At low power mode typical 2uA, At fast mode typical 6.5uA
- @VDD=3V operating voltage :
The response time about 60mS at fast mode, 160mS at low power mode
- Sensitivity can adjust by the capacitance(0~50pF) outside for each touch pad
- Provides direct mode or toggle mode 、 active high or active low by pad option (TOG/AHLB pin).
- After power-on have about 0.5sec stable-time, during the time do not touch the key pad, and the function is disabled
- Auto calibration for life, and the re-calibration period is about 4.0sec, when key has not be touched.

APPLICATION

- Wide consumer products
- Button key replacement

BLOCK DIAGRAM**PACKAGE LIST**

TTP224N-CO8 package type is SOP-8 ,package configuration is below

**PIN DESCRIPTION**

Pin No.	Pin Name	Type	Pad Description
1	TP0	I/O	Touch pad input pin
2	TP1	I/O	Touch pad input pin
3	AHLB	I-PL	Output active high or low option, default:0
4	VDD	P	Positive power supply
5	TOG	I-PL	Output type option, default:0
6	VSS	P	Negative power supply, ground
7	TPQ1	O	Direct output for TP1 touch input pin
8	TPQ0	O	Direct output for TP0 touch input pin

Note : Pin Type

- I => CMOS input only
- I-PL => CMOS input and pull-low resistor
- O => CMOS push-pull output
- I/O => CMOS I/O
- P => Power / Ground

ELECTRICAL CHARACTERISTICS**• Absolute Maximum Ratings**

Parameter	Symbol	Conditions	RATING	Unit
Operating Temperature	Top	—	-40°C ~ +85°C	°C
Storage Temperature	T _{STG}	—	-50°C ~ +125°C	°C
Supply Voltage	VDD	Ta=25°C	VSS-0.3 ~ VSS+6.0	V
Input Voltage	V _{IN}	Ta=25°C	VSS -0.3 to VDD+0.3	V
Human Body Mode	ESD	—	5	KV

Note: VSS symbolizes for system ground

• DC/AC Characteristics : (Test condition at room temperature=25°C)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Operating Voltage	VDD	Regulator enable	2.4	-	5.5	V
Internal Regulator Output	VREG	Regulator enable	2.2	2.3	2.4	V
Operating Current (no load, regulator enable)	I_{op1}	VDD=3V, At low power mode		2		uA
		VDD=3V, At fast mode		6.5		uA
Input Ports	V_{IL}	Input Low Voltage	0	-	0.2	VDD
Input Ports	V_{IH}	Input High Voltage	0.8	-	1.0	VDD
Output Port Sink Current	I_{OL}	VDD=3V, V _{OL} =0.6V	-	8	-	mA
Output Port Source Current	I_{OH}	VDD=3V, V _{OH} =2.4V	-	-4	-	mA
Input Pin Pull-low Resistor	R_{PL}	VDD=3V,		25K		ohm
Output Response Time	T_R	VDD=3V, At fast mode		60		mS
		VDD=3V, At low power mode		160		mS

FUNCTION DESCRIPTION

1. Sensitivity adjustment

The total loading of electrode size and capacitance of connecting line on PCB can affect the sensitivity. So the sensitivity adjustment must according to the practical application on PCB. The TTP224N-CO8 offers some methods for adjusting the sensitivity outside.

1-1 by the electrode size

Under other conditions are fixed. Using a larger electrode size can increase sensitivity.

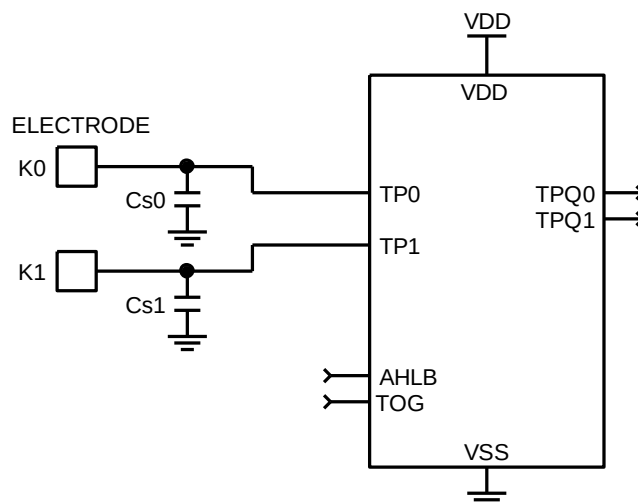
Otherwise it can decrease sensitivity. But the electrode size must use in the effective scope.

1-2 by the panel thickness

Under other conditions are fixed. Using a thinner panel can increase sensitivity. Otherwise it can decrease sensitivity. But the panel thickness must be below the maximum value.

1-3 by the value of Cs0~Cs1 (please see the down figure)

Under other conditions are fixed. Add the capacitors Cs0~Cs1 can fine tune the sensitivity for single key, that lets all key's sensitivity identical. When do not use any capacitor to VSS, the sensitivity is most sensitive. When adding the values of Cs0~Cs1 will reduce sensitivity in the useful range ($0 \leq Cs0 \sim Cs1 \leq 50pF$).



2. Output mode(By TOG, AHLB pad option)

The TTP224N-CO8 outputs(TPQ0~TPQ1) has direct mode active high or low by AHLB pad option, has toggle mode by TOG pad option.

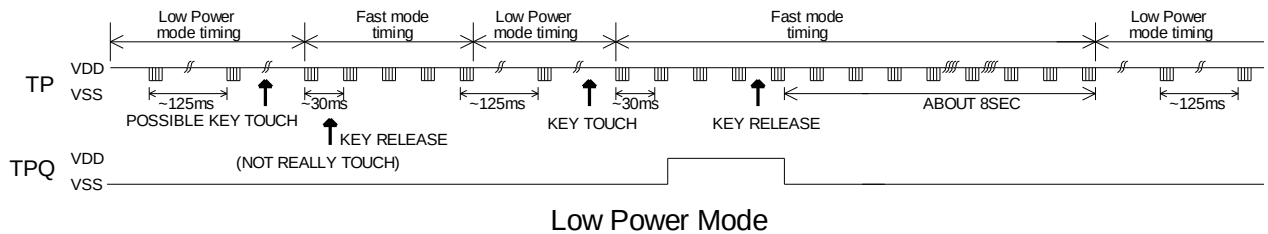
TOG	AHLB	Pad TPQ0~TP1 option features	Remark
0	0	Direct mode, CMOS active high output	Default
0	1	Direct mode, CMOS active low output	
1	0	Toggle mode, COMS output, Power on state=0	
1	1	Toggle mode, COMS output, Power on state=1	

3. Low power mode

The TTP224N-CO8 is Low Power mode. It will be saving power. When detecting key touch, it will switch to Fast mode. Until the key touch is released and will keep a time about 8sec. Then it returns to Low Power mode.

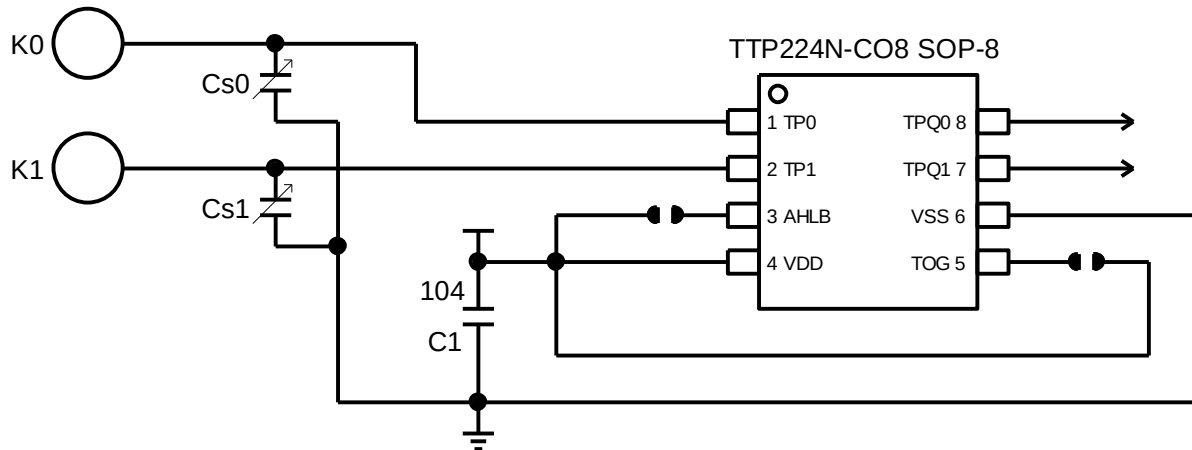
The states and timing please see below figure.

TTP224N-CO8 timing diagram :



APPLICATION CIRCUIT

TTP224N-CO8 APPLICATION



Option table:

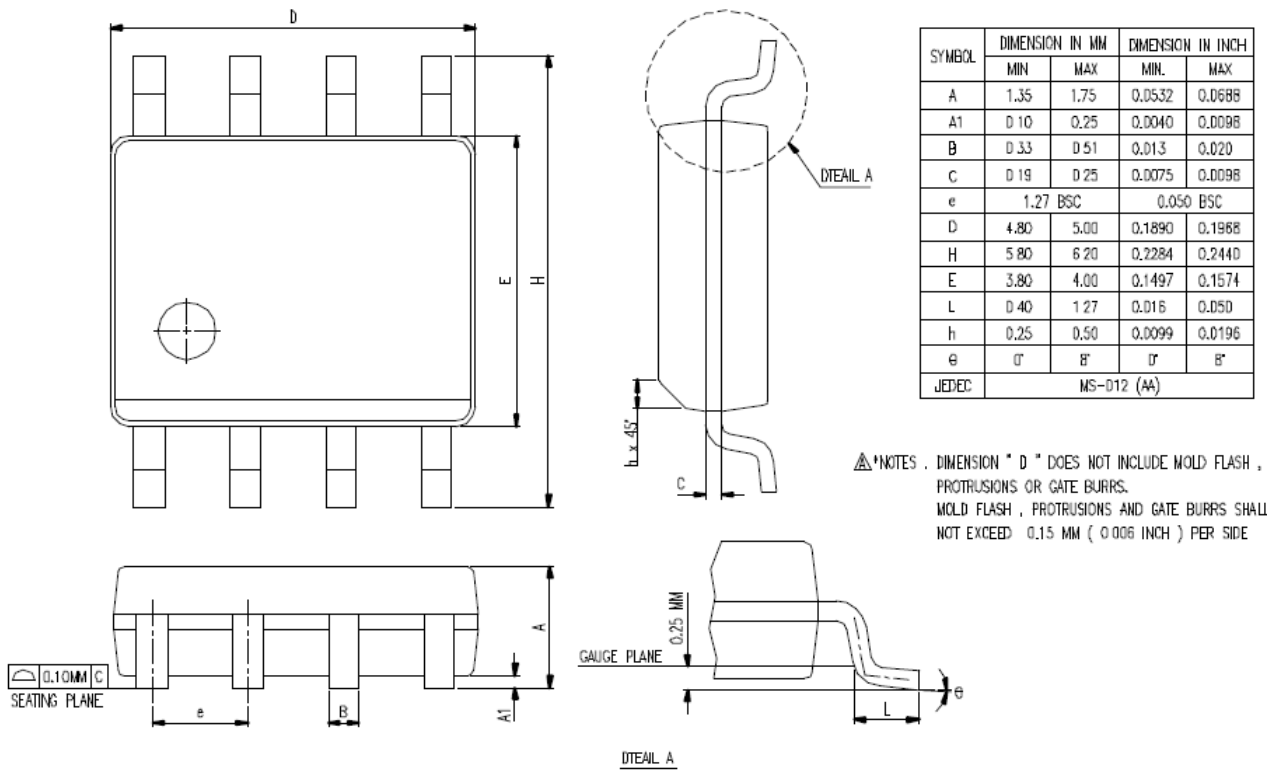
Output mode:

TOG	AHLB	Pad TPQ0~TP1 option features
open	open	Direct mode, CMOS active high output
open	VDD	Direct mode, CMOS active low output
VDD	open	Toggle mode, COMS output, Power on state=0
VDD	VDD	Toggle mode, COMS output, Power on state=1

- PS : 1. On PCB, the length of lines from touch pad to IC pin shorter is better.
And the lines do not parallel and cross with other lines.
- The material of panel covering on the PCB can not include the metal or the electric element. The paints on the surfaces are the same.
 - The C1 capacitor must be used between VDD and VSS; and should be routed with very short tracks to the device's VDD and VSS pins .
 - The capacitance Cs0~Cs1 can be used to adjust the sensitivity. The value of Cs0~Cs1 use smaller, then the sensitivity will be better. The sensitivity adjustment must according to the practical application on PCB. The range of Cs0~Cs1 value are 0~50pF.
 - The sensitivity adjustment capacitors (Cs0~Cs1) must use smaller temperature coefficient and more stable capacitors. Such are X7R, NPO for example.
So for touch application, recommend to use NPO capacitor, for reducing that the temperature varies to affect sensitivity.

Package Type: SOP-8

Package Outline Dimension



ORDER INFORMATION

- a. Package form: TTP224N-CO8
- b. Chip form: TCP224N
- c. Wafer base: TDP224-02

REVISE HISTORY

- 1. 2009/06/30
 - Original version : V1.0
- 2. 2009/08/25 ➔ V2.0
 - Add the TTP224N-CO8
- 3. 2010/06/08 ➔ V3.0
 - Add the TTP224L-CO8
- 4. 2014/06/05 ➔ V3.1
 - Remove TTP224-CO8 and TTP224L-CO8 function